

## General Description

FSMOS<sup>®</sup> MOSFET is based on Oriental Semiconductor's unique device design to achieve low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The high  $V_{th}$  series is specially designed to use in motor control systems with driving voltage of more than 10V.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



## Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

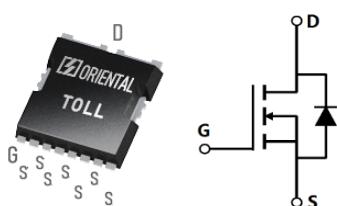
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS}$	150	V
$I_D$ , pulse	720	A
$R_{DS(ON)}$ , max @ $V_{GS}=10V$	5	mΩ
$Q_g$	149	nC

## Marking Information

Product Name	Package	Marking
SFS15R05TNF	TOLL	SFS15R05TN

## Package & Pin information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	150	V
Gate-source voltage	$V_{GS}$	$\pm 20$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	180	A
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	720	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	180	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	720	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	450	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	135	mJ
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.27	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	150			V	$V_{GS}=0 \text{ V}, I_D=250 \mu\text{A}$
Gate threshold voltage	$V_{GS(\text{th})}$	3		4.5	V	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.6	5	$\text{m}\Omega$	$V_{GS}=10 \text{ V}, I_D=60 \text{ A}$
Gate-source leakage current	$I_{GSS}$			100	$\text{nA}$	$V_{GS}=20 \text{ V}$
				-100		$V_{GS}=-20 \text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=120 \text{ V}, V_{GS}=0 \text{ V}$
Gate resistance	$R_G$		1.2		$\Omega$	$f=1 \text{ MHz}, \text{Open drain}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		13467		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =25 V, f=100 kHz
Output capacitance	C <sub>oss</sub>		4347		pF	
Reverse transfer capacitance	C <sub>rss</sub>		295		pF	
Turn-on delay time	t <sub>d(on)</sub>		43		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =80 V, R <sub>G</sub> =2 Ω, I <sub>D</sub> =40 A
Rise time	t <sub>r</sub>		37		ns	
Turn-off delay time	t <sub>d(off)</sub>		74		ns	
Fall time	t <sub>f</sub>		26		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		149		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =80 V, I <sub>D</sub> =40A
Gate-source charge	Q <sub>gs</sub>		56		nC	
Gate-drain charge	Q <sub>gd</sub>		28		nC	
Gate plateau voltage	V <sub>plateau</sub>		5.4		V	

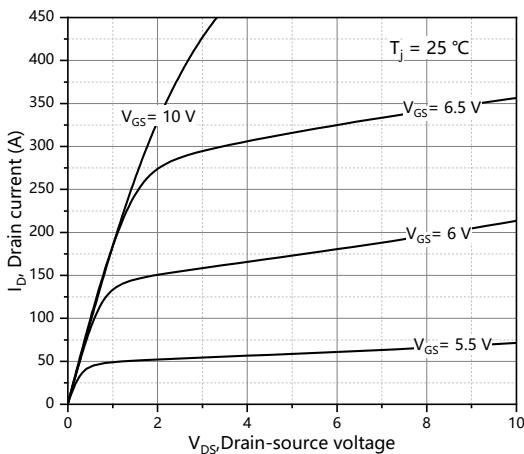
### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>s</sub> =20 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		130		ns	V <sub>R</sub> =80 V, I <sub>s</sub> =40 A, di/dt=100 A/μs
Reverse recovery charge	Q <sub>rr</sub>		497		nC	
Peak reverse recovery current	I <sub>rrm</sub>		6.2		A	

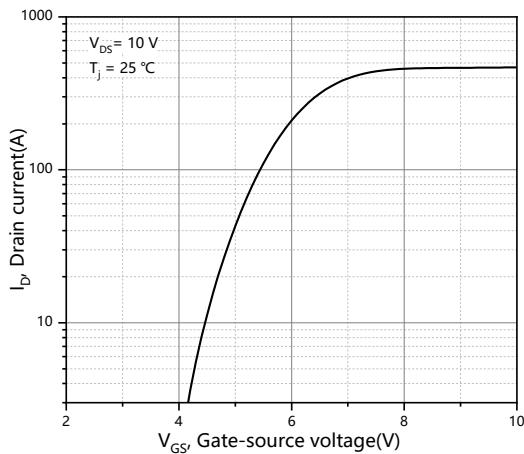
### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) V<sub>DD</sub>=50V, V<sub>GS</sub>=10 V, L=0.3 mH, starting T<sub>j</sub>=25 °C.

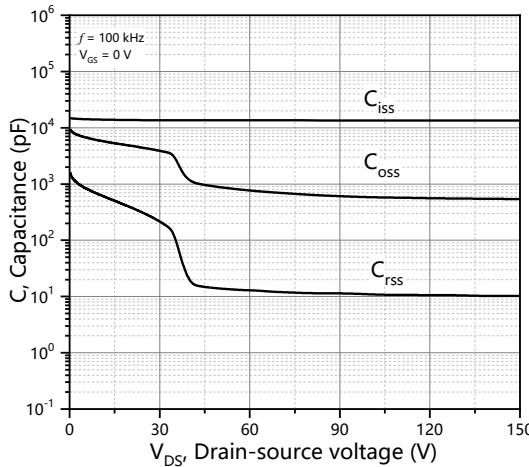
### Electrical Characteristics Diagrams



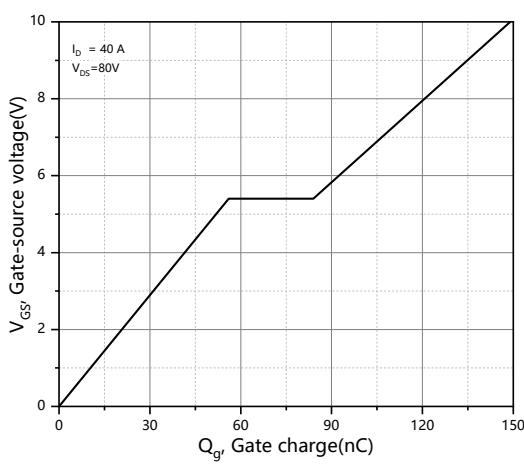
**Figure 1. Typ. output characteristics**



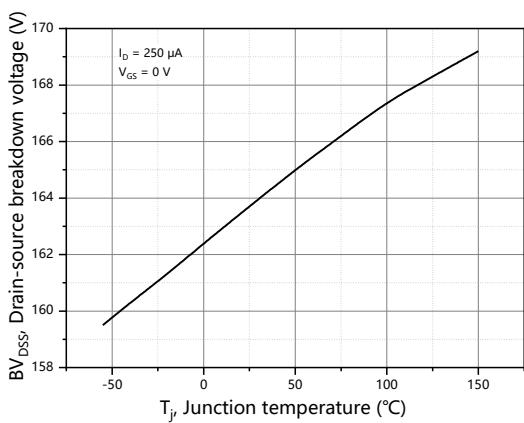
**Figure 2. Typ. transfer characteristics**



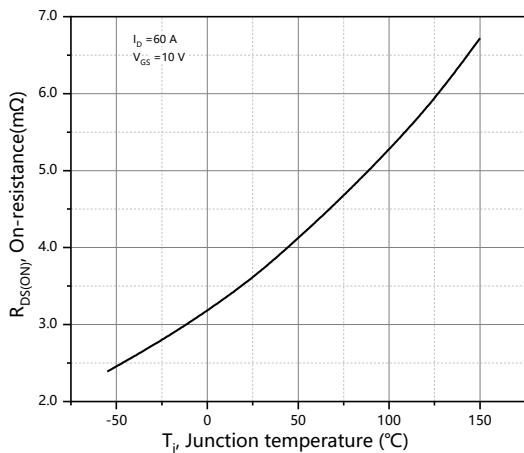
**Figure 3. Typ. capacitances**



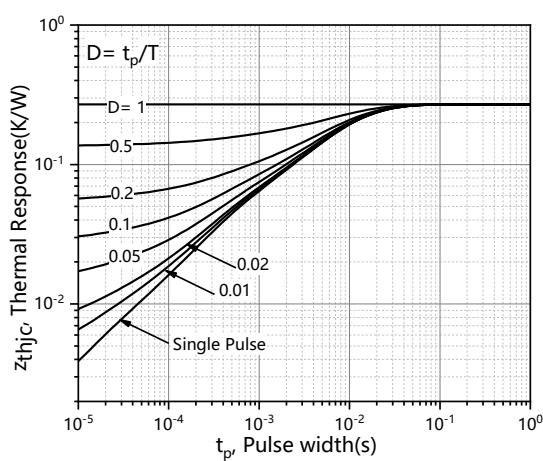
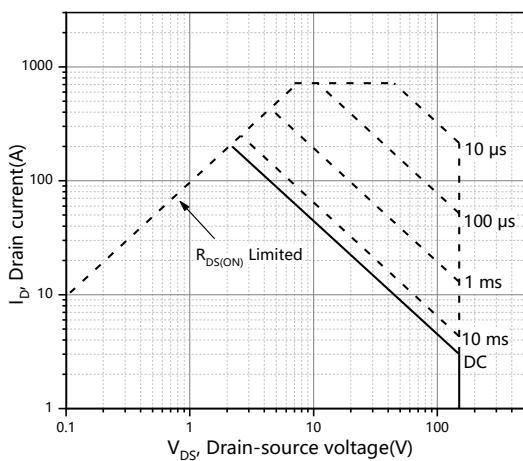
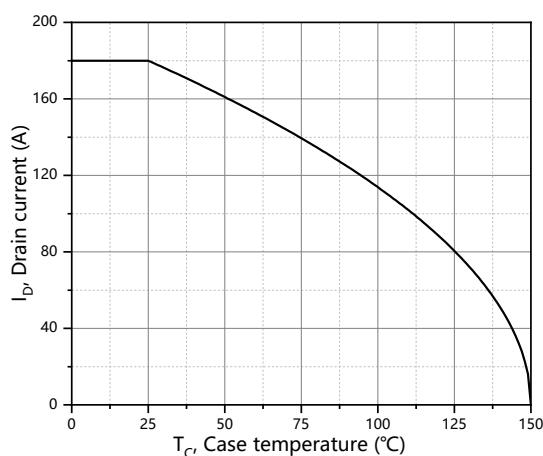
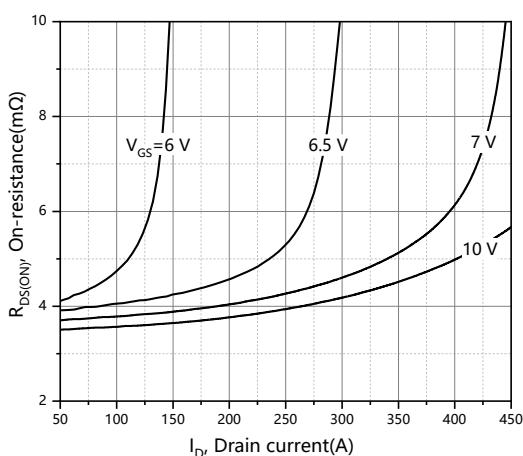
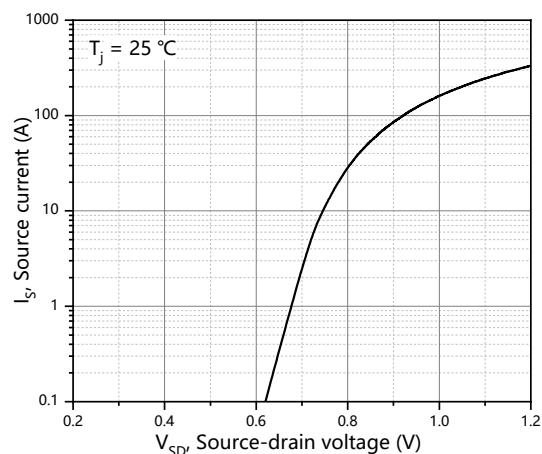
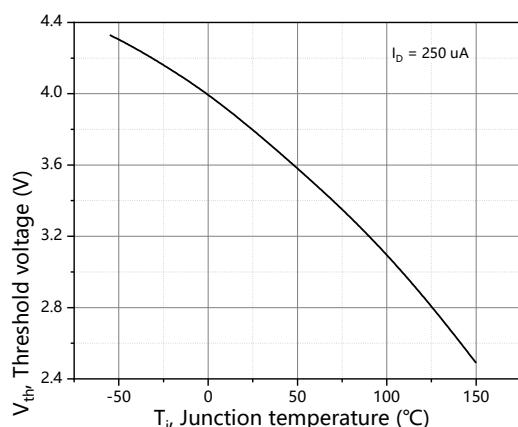
**Figure 4. Typ. gate charge**



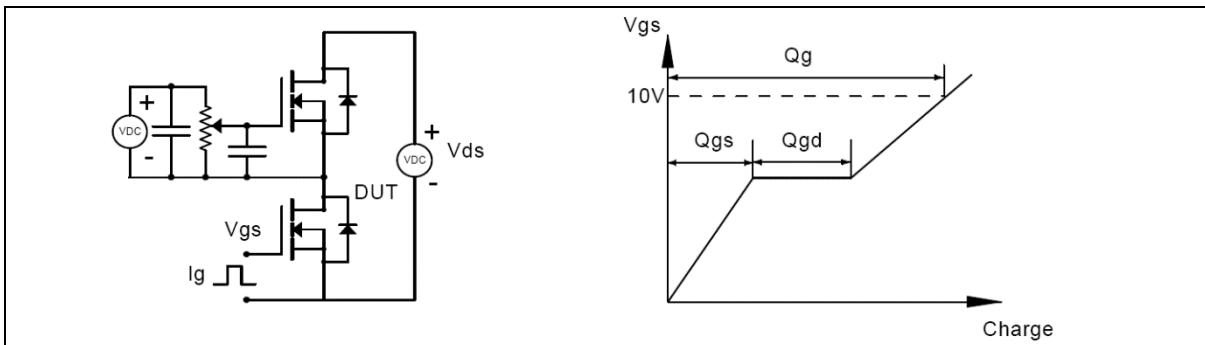
**Figure 5. Drain-source breakdown voltage**



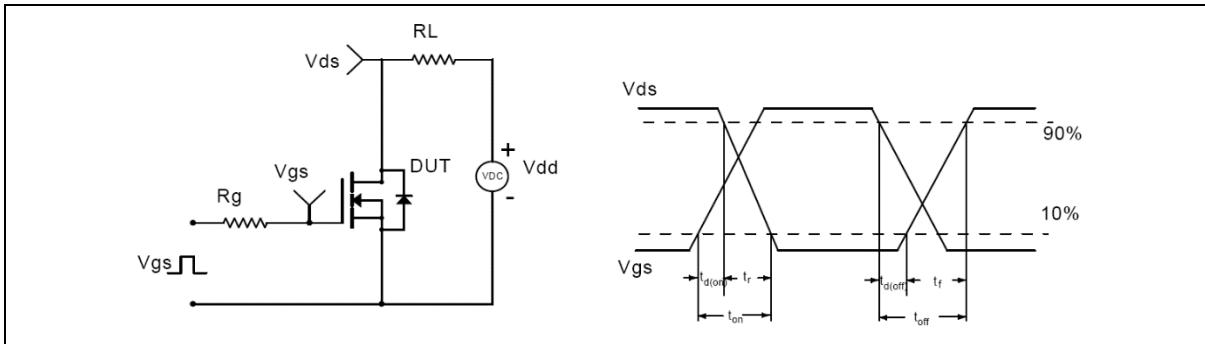
**Figure 6. Drain-source on-state resistance**



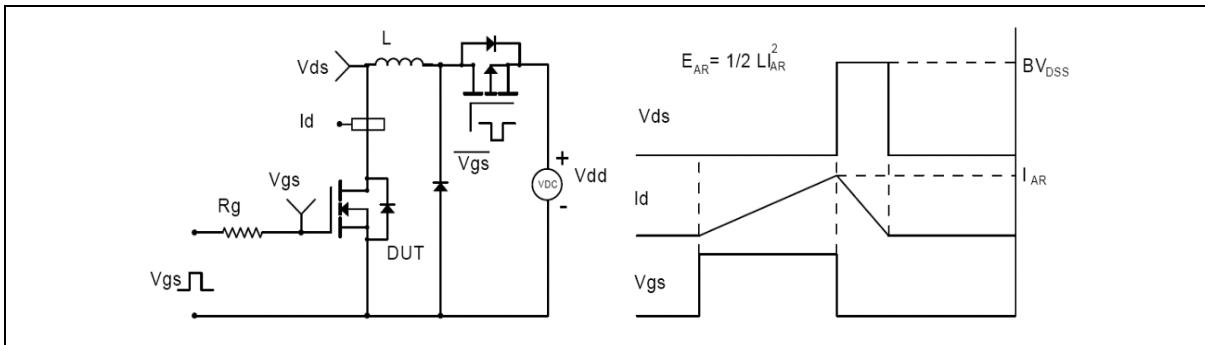
### Test circuits and waveforms



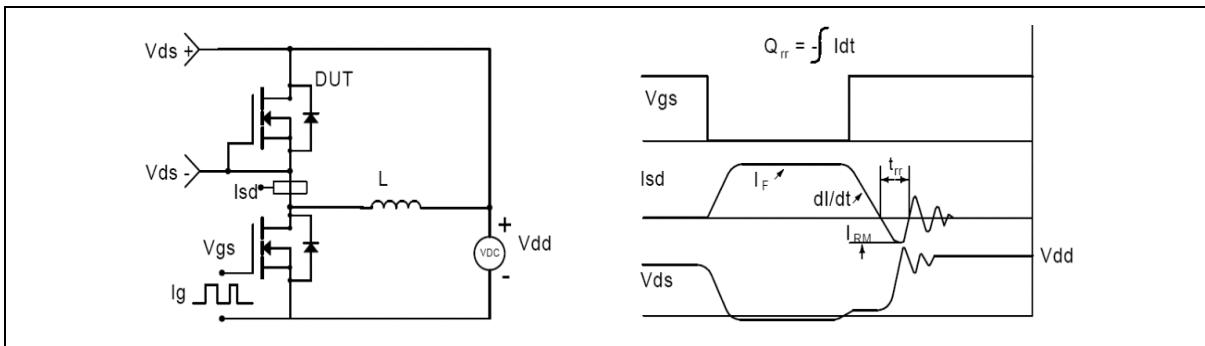
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

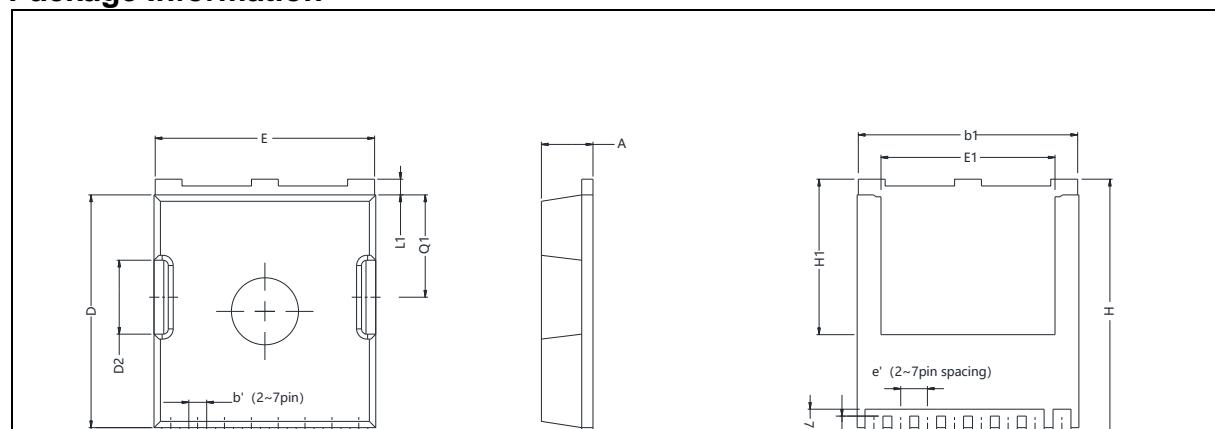


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

## Package Information



Symbol	mm		
	Min	Nom	Max
A	2.15	2.30	2.45
b	0.75	0.75	0.85
b'	0.70	0.70	0.80
b1	9.65	9.80	9.95
C	0.45	0.50	0.60
D	10.18	10.38	10.58
D2	3.15	3.30	3.45
E	9.70	9.90	10.10
E1	7.95	8.10	8.25
e	BSC 1.225		
e'	BSC 1.20		
Q1	4.40	4.55	4.70
H	11.48	11.68	11.88
H1	6.80	6.95	7.10
L	1.60	1.80	2.00
L1	0.50	0.70	0.90
L2	0.48	0.60	0.72
L4	1.00	1.15	1.30

Version 1: TOLL-P package outline dimension

## Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TOLL-P	1200	1	1200	5	6000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS15R05TNF	TOLL	yes	yes	yes

## Legal Disclaimer

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