General Description

OST50N65KTMF uses advanced Oriental-Semi's patented Trident-Gate Bipolar Transistor (TGBT™) technology to provide extremely low V_{CE}(sat), low gate charge, and excellent switching performance. This device is suitable for mid to high range switching frequency converters.

Features

- Advanced TGBT™ technology
- Excellent conduction and switching loss
- Excellent stability and uniformity
- Fast and soft antiparallel diode

Applications

- PV inverters
- Induction converters
- Uninterruptible power supplies

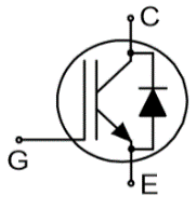
Key Performance Parameters

Parameter	Value	Unit
$V_{CES, min}$ @ 25°C	650	V
Maximum junction temperature	175	°C
$I_{C, pulse}$	200	A
$V_{CE(sat), typ}$ @ V_{GE}=15V	1.65	V
Q_g	78	nC

Marking Information

Product Name	Package	Marking
OST50N65KTMF	TO263	OST50N65KTM

Package & Pin Information



Absolute Maximum Ratings at T_{vj}=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Collector emitter voltage	V_{CES}	650	V
Gate emitter voltage	V_{GES}	±20	V
Transient gate emitter voltage, T_P≤10μs, D<0.01		±30	V
Continuous collector current[1], T_C=25℃	I_C	80	A
Continuous collector current[1], T_C=100℃		50	A
Pulsed collector current[2], T_C=25℃	$I_{C, pulse}$	200	A
Diode forward current[1], T_C=25℃	I_F	80	A
Diode forward current[1], T_C=100℃		50	A
Diode pulsed current[2], T_C=25℃	$I_{F, pulse}$	200	A
Power dissipation[3], T_C=25℃	P_D	250	W
Power dissipation[3], T_C=100℃		125	W
Operation and storage temperature	T_{stg}, T_{vj}	-55 to 175	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
IGBT thermal resistance, junction-case	$R_{\theta JC}$	0.60	°C/W
Diode thermal resistance, junction-case	$R_{\theta JC}$	0.85	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	40	°C/W

Electrical Characteristics at T_{vj}=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Collector-emitter breakdown voltage	$V_{(BR)CES}$	650			V	V_{GE}=0 V, I_C=0.5 mA
Collector-emitter saturation voltage	$V_{CE(sat)}$		1.65	1.85	V	V_{GE}=15 V, I_C=50 A T_{vj}=25°C
			1.95		V	V_{GE}=15 V, I_C=50 A, T_{vj} =125°C
			2.10			V_{GE}=15 V, I_C=50 A, T_{vj} =175°C
Gate-emitter threshold voltage	$V_{GE(th)}$	4.0	5.0	6.0	V	V_{CE}=V_{GE}, I_D=0.5 mA
Diode forward voltage	V_F		1.75	2.05	V	V_{GE}=0 V, I_F=50 A T_{vj} =25°C
			1.65			V_{GE}=0 V, I_F=50 A, T_{vj} =125°C
			1.20			V_{GE}=0 V, I_F=50 A, T_{vj} =175°C
Gate-emitter leakage current	I_{GES}			100	nA	V_{CE}=0 V, V_{GE}=20 V
Zero gate voltage collector current	I_{CES}			10	μA	V_{CE}=650 V, V_{GE}=0 V

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{ies}		4295		pF	V_{GE}=0 V, V_{CE}=25 V, f=100 kHz
Output capacitance	C_{oes}		114		pF	
Reverse transfer capacitance	C_{res}		3.9		pF	
Turn-on delay time	$t_{d(on)}$		34		ns	V_{GE}=15 V, V_{CC}=400 V, R_G=10 Ω, I_C=50 A
Rise time	t_r		58		ns	
Turn-off delay time	$t_{d(off)}$		91		ns	
Fall time	t_f		105		ns	
Turn-on energy	E_{on}		1.72		mJ	
Turn-off energy	E_{off}		1.21		mJ	
Turn-on delay time	$t_{d(on)}$		31		ns	V_{GE}=15 V, V_{CC}=400 V, R_G=10 Ω, I_C=25 A
Rise time	t_r		26		ns	
Turn-off delay time	$t_{d(off)}$		126		ns	
Fall time	t_f		78		ns	
Turn-on energy	E_{on}		0.70		mJ	
Turn-off energy	E_{off}		0.69		mJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		78		nC	V_{GE}=15 V, V_{CC}=520 V, I_C=50 A
Gate-emitter charge	Q_{ge}		42		nC	
Gate-collector charge	Q_{gc}		12		nC	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode reverse recovery time	t_{rr}		114		ns	V_R=400 V, I_F=50 A, di_F/dt=500 A/μs T_{vj} = 25°C
Diode reverse recovery charge	Q_{rr}		960		nC	
Diode peak reverse recovery current	I_{rrm}		15.4		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.

Electrical Characteristics Diagrams

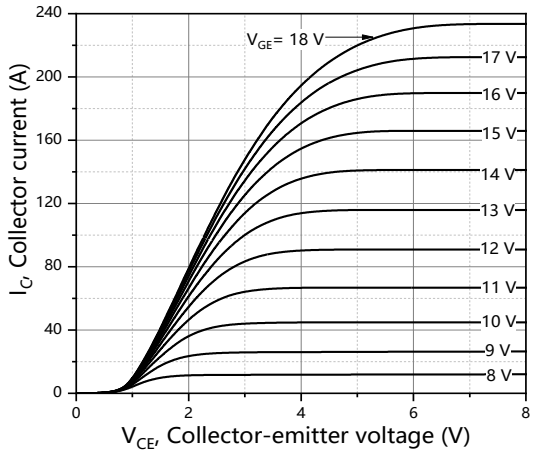


Figure 1. Typical output characteristics
(T_{vj}=25°C)

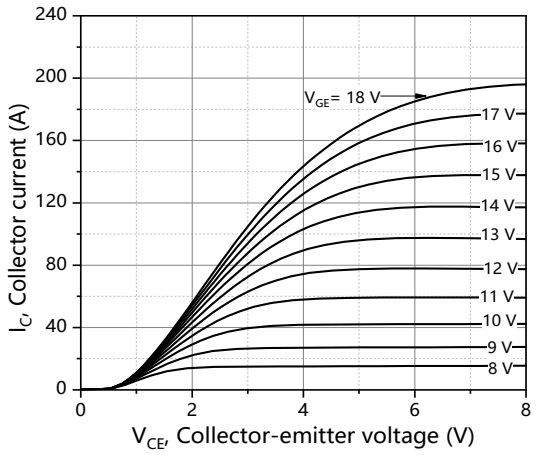


Figure 2. Typical output characteristics
(T_{vj}=150°C)

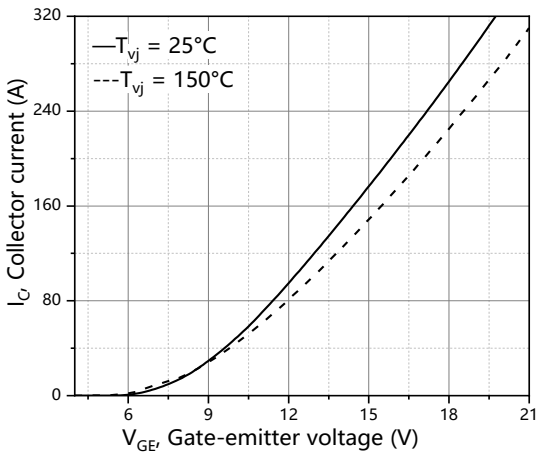


Figure 3. Typical transfer characteristics
(V_{CE}=20V)

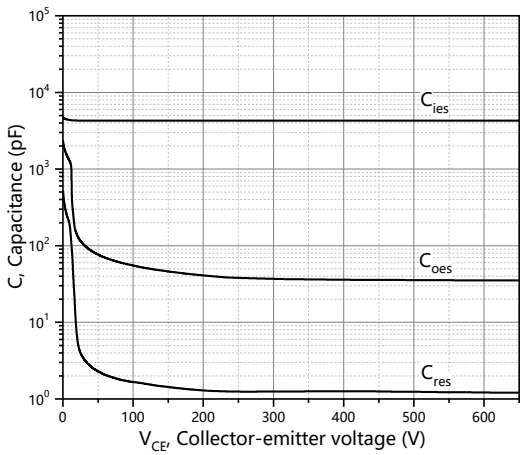


Figure 4. Typical capacitance
(V_{GE}=0V, f=100 kHz)

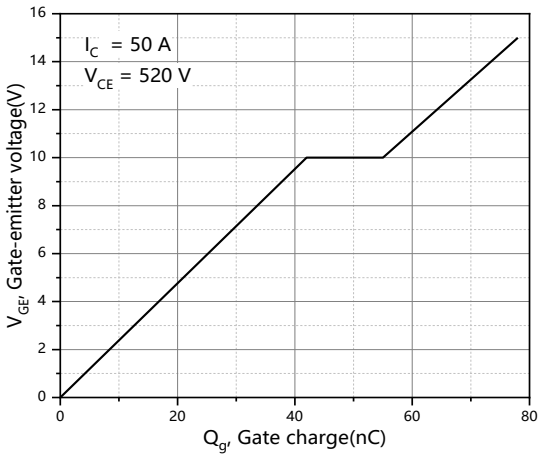


Figure 5. Typical gate charge

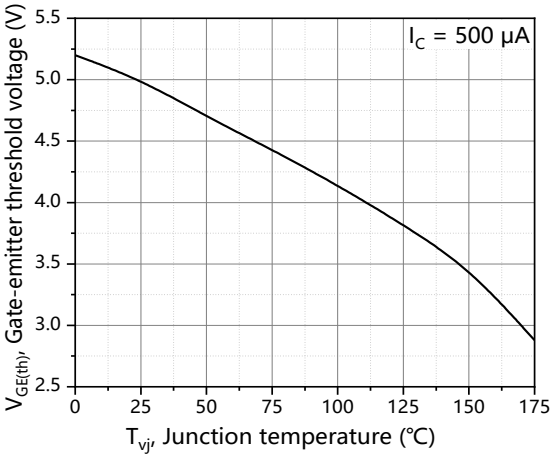


Figure 6. Gate-emitter threshold voltage

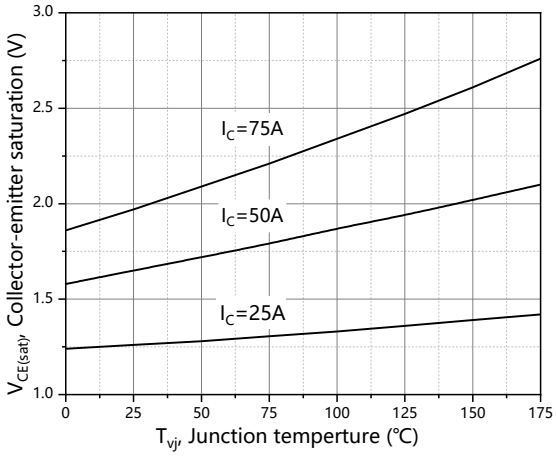


Figure 7. Typical collector-emitter voltage

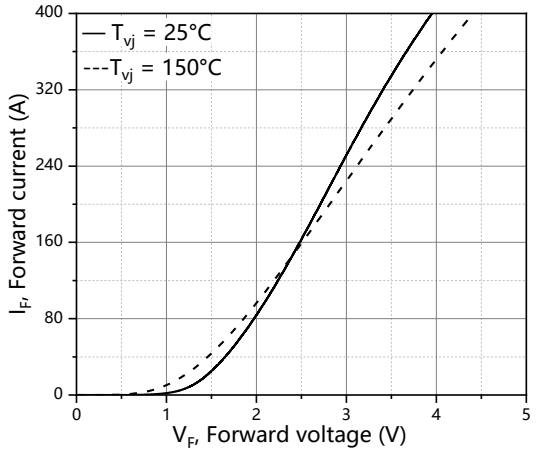


Figure 8. Forward characteristic of diode

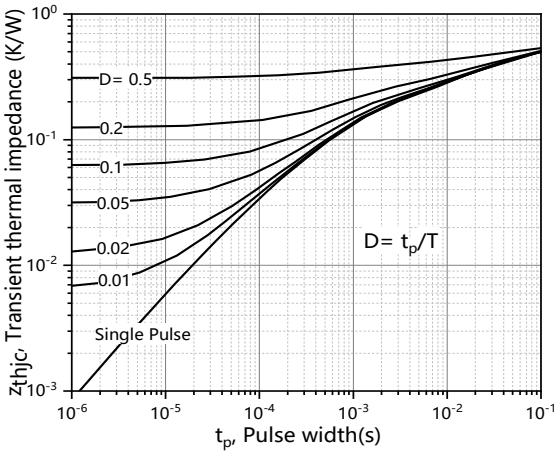


Figure 9. IGBT transient thermal impedance

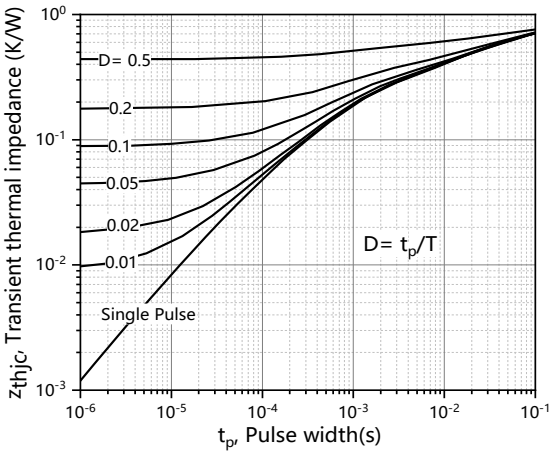
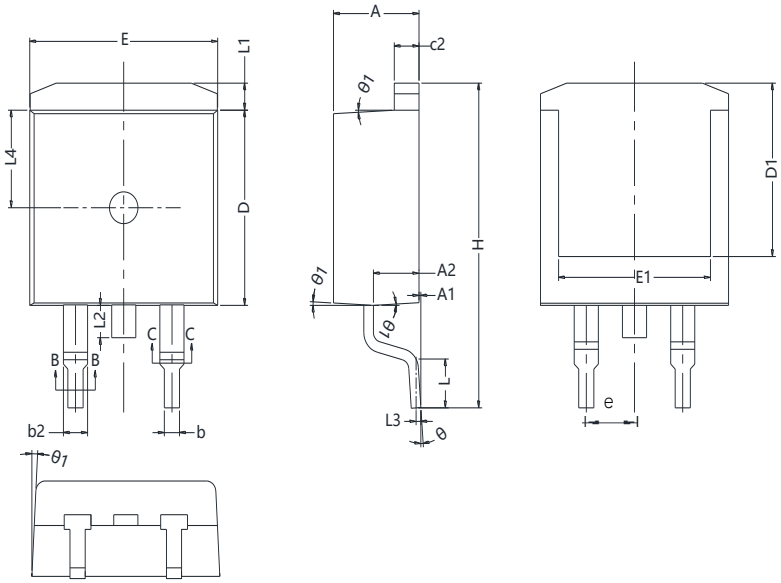


Figure 10. Diode transient thermal impedance

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	0.00	0.10	0.25
A2	2.20	2.40	2.60
b	0.76	-	0.89
b2	1.23	-	1.37
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00	-	-
E	9.80	9.90	10.00
E1	7.80	-	-
e	2.54 BSC		
H	14.90	15.30	15.70
L	2.00	2.30	2.60
L1	1.17	1.27	1.40
L2	-	-	1.75
L3	0.25 BSC		
L4	4.60 REF		
θ	0°	-	8°
θ1	1°	3°	5°

Version 1: TO263-J package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO263-J	800	1	800	10	8000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OST50N65KTMF	TO263	yes	yes	yes

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives (www.orientalsemi.com).

ORIENTAL SEMICONDUCTOR

A Power Semiconductor Innovator